

Department of Information and Communication Technology (ICT)

SCHOOL OF TECHNOLOGY (SoT), PDPU

Board of Studies (BoS) Meeting of the ICT department - June 2020

Minutes of the Meeting

Date: 9-June-2020

Time: 10 am to 12.30 pm

This meeting was conducted on Skype online mode.

Members present:

<p>BoS Internal members: Dr. Mehul S Raval, Chairman BoS, HoD ICT Dept. Dr. Paawan Sharma Dr. Raju Ranjan Dr. Jigarkumar Shah</p> <p>BoS Student members: Aanchal Vatsraj Kalgee Kotak Krushang Satani Nirbhay Parmar</p>	<p>Faculty members of ICT department: Dr. Mazad Zaveri Dr. Mohendra Roy Dr. Ganga Prasad Pandey Dr. Hardik Patel</p>
<p>BoS External members: Dr. Debajyoti Dhar, ISRO, Ahmedabad Dr. Bishwajit Mishra, DAIICT, Gandhinagar Dr. Amit Nanavati, IIT-Delhi Mr. Nilesh Ranpura, eInfochips, Ahmedabad</p>	

Special Invitee Members:

Prof. Sunil Khanna, Director-SoT

Prof. D. M. Parikh, Dean (FoET)

Prof. T.P. Singh, Director of Academic Affairs

Following is a summary of the discussions/suggestions/comments of the BoS:

- The BoS approves the ICT department's: Vision, Mission, PEOs, PSOs, POs.
- If possible, move the course Analog Circuit Design to higher semester (For example, swap it with OOP).
 - The two courses Analog Circuit Design and Digital Logic Design & HDL should not be in the same semester, because it would be a considerable load for the students if both are offered in the same semester.
- For course Digital Logic Design & HDL, introduce a few FPGA based experiments in the lab. FPGA boards (lower end) can be procured by the ICT department. These boards can also be used for a small project in the course Computer Organization and Design.
- BoS suggested that (first year course) Elements of Civil Engg and Solid Mechanics should be removed for the BTech ICT program. It may not be very relevant for ICT.
- If possible (in terms of the constraints), DBMS could be moved to an earlier semester, as per suggestions from the student members.
- BoS suggested that, for providing real freedom/exposure of electives (in the ICT domain, which is multidisciplinary), the limit of minimum 20 students/registration should be lowered. This is because, there will always be a small set of students interested in one of the smaller/concentrated areas of ICT.
- In course Operating Systems, I/O management, device interfacing, can be introduced.
- Open electives are nicely designed (because they don't need extensive pre-requisites).
- BoS suggested that new ICT curriculum is balanced, and nicely covers all the three verticals of ICT (Computer Science and Engg, Communication and Signal Processing, and Electronics - Embedded Sys. and VLSI)
 - It would create good employability and provide good exposure.
- The BoS approves the new (AICTE aligned) BTech ICT program curriculum (2020-2024): Structure, courses and related syllabi, which will be effective from July 2020 (for incoming batch).

Actions taken based on BoS suggestions:

- Analog Circuit Design is to be moved to Sem-4 and Object Oriented Programming to be moved to Sem-3.
- DBMS is to be moved to Sem-4 and CCN moved to Sem-6.
- Course content for Operating Systems to include topics: I/O device management, interface, device drivers.